Unit: mm

TOSHIBA Transistor Silicon PNP Epitaxial Type

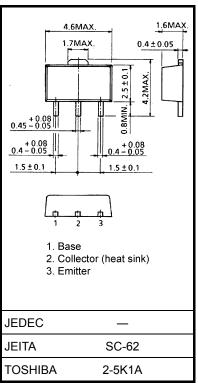
# 2SA2060

High-Speed Switching Applications DC-DC Converter Applications Strobe Applications

- High DC current gain:  $h_{FE} = 200 \text{ to } 500 \text{ (I}_{C} = -0.3 \text{ A)}$
- Low collector-emitter saturation voltage: V<sub>CE (sat)</sub> = −0.2 V (max)
- High-speed switching: t<sub>f</sub> = 90 ns (typ.)

#### Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Collector-base voltage		V <sub>CBO</sub>	-50	V	
Collector-emitter voltage		V <sub>CEO</sub>	-50	V	
Emitter-base voltage		V <sub>EBO</sub>	-7	V	
Collector current	DC	IC	-2.0	Α	
	Pulse	I <sub>CP</sub>	-3.5		
Base current		ΙΒ	-200	mA	
Collector power dissipation	t = 10 s	PC	2.5	W	
	DC	(Note 1)	1.0		
Junction temperature		Tj	150	°C	
Storage temperature range		T <sub>stg</sub>	−55 to 150	°C	



Weight: 0.05 g (typ.)

Note 1: Mounted on an FR4 board (glass epoxy, 1.6 mm thick, Cu area: 645 mm<sup>2</sup>)

Note 2: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

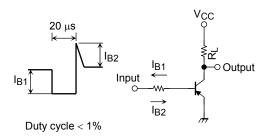
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).



### **Electrical Characteristics (Ta = 25°C)**

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit	
Collector cut-off current		I <sub>CBO</sub>	$V_{CB} = -50 \text{ V}, I_E = 0$	_	_	-100	nA	
Emitter cut-off current		I <sub>EBO</sub>	$V_{EB} = -7 \text{ V}, I_{C} = 0$	_	_	-100	nA	
Collector-emitter breakdown voltage		V (BR) CEO	$I_C = -10 \text{ mA}, I_B = 0$	-50	_	_	٧	
DC current gain		h <sub>FE</sub> (1)	V <sub>CE</sub> = -2 V, I <sub>C</sub> = -0.3 A	200	_	500		
		h <sub>FE</sub> (2)	V <sub>CE</sub> = -2 V, I <sub>C</sub> = -1.0 A	100	_	_		
Collector-emitter saturation voltage		V <sub>CE</sub> (sat)	I <sub>C</sub> = -1.0 A, I <sub>B</sub> = -0.033 A	_	_	-0.2	V	
Base-emitter saturation voltage		V <sub>BE</sub> (sat)	I <sub>C</sub> = -1.0 A, I <sub>B</sub> = -0.033 A	_	_	-1.1	V	
Collector output capacitance		C <sub>ob</sub>	V <sub>CB</sub> = −10 V, I <sub>E</sub> = 0, f = 1 MHz	_	20	_	pF	
Switching time	Rise time	t <sub>r</sub>	See Figure 1 circuit diagram.	_	60	_		
	Storage time	t <sub>stg</sub>	$V_{\rm CC} \approx -30 \text{ V}, R_{\rm L} = 30 \Omega$	_	250	_	ns	
	Fall time	t <sub>f</sub>	$I_{B1} = 33 \text{ mA}, I_{B2} = 33 \text{ mA}$	_	90	_		

## Marking



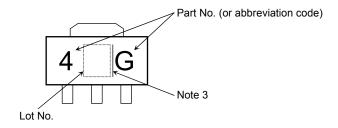


Figure 1 Switching Time Test Circuit & Timing Chart

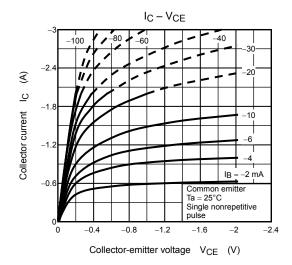
Note 3: A line to the right of a Lot No. identifies the indication of product Labels.

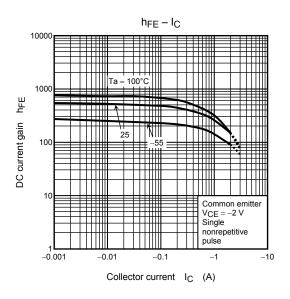
Without a line: [[Pb]]/INCLUDES > MCV

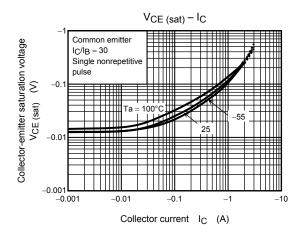
With a line: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

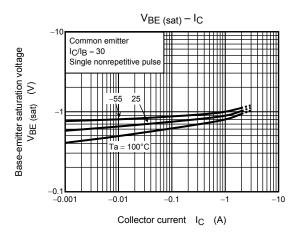
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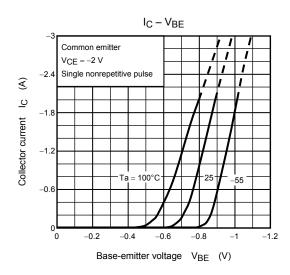
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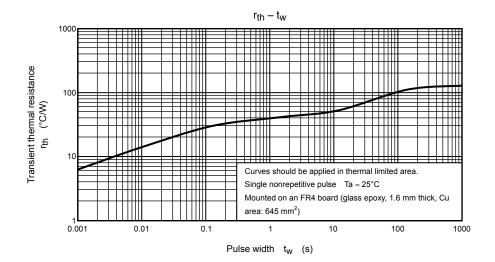


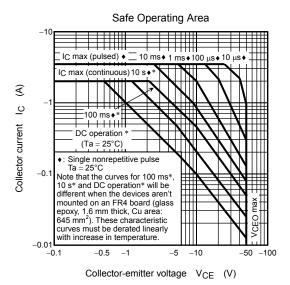






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